SEARCHED

Class	Sub.	Date	Exmr.
703	19	11-26	DmC
703	28	11-27	
709	104	11-27	
709	218	12-3	
	219		
	220		
	221		
	202		
	107		
	400		
	314		
updated		6-19-05	DmC
716	1	1-14-04	DmC
	6		
	6		
	18		
703	19	1-14-04	DmC
	28		

SB
1/7/00

INTERFERENCE SEARCHED

Class	Sub.	Date	Exmr.

SEARCH NOTES
(INCLUDING SEARCH STRATEGY)

	Date	Exmr.
Clock Synchronization hardware description language Circuit simulator Simulation module thread token Verilog VHDL clock	11-26	DmC
IEEE search Synchronization hardware simulation semaphore halting execution distributed simulation	11-27	
Simulation module Synchronization points Simulation clock domain Synchronization	12-4	
ACM Synchronization simulation clock		
device under test DUT Test bench	12-7	
Clock cycles simulation execution time	6-19	DmC
EAST VERILOG VHDL DUT Clock credit Clock token Clock domain Simulation module	1-13-04	DmC
actors Clock credit execution time Simulation manager	1-13-04	DmC